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APPLICANT William T. Taylor, Jr. GROUP ART UNIT: 2823
APPLN. NO.: 10/670,634 EXAMINER: William M. Brewster
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TITLE: SEMICONDUCTOR PROCESS FOR DISPOSABLE SIDEWALL
SPACERS

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AMENDMENT UNDER 37 CFR 1.312

Commissioner for Patents
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Sir:

This paper is filed contemporaneous with the filing of the issue fee for this
application. Response to the comments provided below is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079
(Freescale Semiconductor, Inc.) for any fees owed in connection with this
application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.)
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